

**JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE**  
Nustulapur, Karimnagar – 505 481.

**Date: 16-09-2015**

**End Examination Time Table of  
M.Tech. I Year I Sem. (R09-Supple) October-2015**

<b>Date &amp; Day</b>	<b>CSE-58</b>	<b>SE-25</b>	<b>CS-05</b>	<b>CNIS-78</b>
05-10-2015 (Monday)	Advanced Problem Solving	Software Requirement Estimation	Advanced Problem Solving	Network Programming
07-10-2015 (Wednesday)	Computer System Design	Software Process and Project Management	Computer System Design	Distributed Computing
09-10-2015 (Friday)	Embedded Systems	Distributed Database [Elect-II]	Embedded Systems	Information Security-I
13-10-2015 (Tuesday)	JAVA and Web Technologies	JAVA and Web Technologies	JAVA and Web Technologies	JAVA and Web Technologies [Elect-I]
15-10-2015 (Thursday)	Object Oriented Modeling [Elect-I]	Object Oriented Modeling [Elect-I]	Object Oriented Modeling [Elect-I]	Data Warehousing & Mining [Elect-II]
17-10-2015 (Saturday)	***	Advanced Computer Network [Elect-I]	***	Advanced Computer Networks
	Advanced Compiler Design	***	Software Design & Engineering	***

Verified by

CSE-HOD

Officer-In-charge of Exam Branch

DIRECTOR

PRINCIPAL

**JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE**  
Nustulapur, Karimnagar – 505 481.

**Date: 02-04-2013**

**End Examination Time Table of  
M.Tech. I Year I Sem. (R09-Supple) October-2015**

<b>Date &amp; Day</b>	<b>VLSI – DESIGN-57</b>	<b>EMB – SYS-55</b>	<b>DSCE-06</b>	<b>PE-43</b>
22-04-2012 (Monday)	Digital System Design	Digital System Design [Elect-II]	Digital System Design	Machine Modeling and Analysis
24-04-2012 (Wednesday)	VLSI Technology & Design	VLSI Technology & Design	VLSI Technology & Design	Analysis of Power Electronic Converters
26-04-2012 (Friday)	***	Embedded Real Time Operating Systems	Advanced Data Communications	***
29-04-2012 (Monday)	Algorithms for VLSI Design & Automation	Advanced Computer Networks	***	Power Electronic Control of DC Drives
01-05-2012 (Wednesday)	CPLD and FPGA Architectures and Applications	***	CPLD and FPGA Architectures and Applications	Modern Control Theory
04-05-2012 (Saturday)	Microcontrollers for Embedded System	Microcontrollers for Embedded System Design	Microcontrollers for Embedded System Design	Dynamics of Electrical Machines [Elect-II]
07-05-2012 (Tuesday)	Advanced Digital Signal Processing	Advanced Digital Signal Processing [Elect-I]	Advanced Digital Signal Processing [Elect-I]	Modern Power Electronics [Elect-I]

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ECE-HOD

EEE-HOD

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**JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE-27**  
**Nustulapur, Karimnagar – 510 481.**

**Date: 14-09-2015**

**End Examination Time Table of**  
**M.Tech. I Year I Sem. (R13-Supple)-October-2015**  
**Academic Year 2014-15**

**Examination Timings-02-00 PM To 05-00 PM**

<b>Date &amp; Day</b>	<b>CSE-58</b>	<b>CS-05</b>	<b>SE-25</b>	<b>CNIS-78</b>
05-10-2015 (Monday)	Advanced Data Structures & Algorithms	Advanced Data Structures & Algorithms	Advanced Data Structures & Algorithms	Advanced Data Structures & Algorithms
07-10-2015 (Wednesday)	Computer System Design	Computer System Design	Software Development Methodologies	Principles of Information Security
09-10-2015 (Friday)	Distributed Systems	Software Process & Project Management	Software Requirements & Estimation	Advanced Computer Networks
13-10-2015 (Tuesday)	Advanced Operating Systems	Advanced Operating Systems	Web Technologies & Services	Web Technologies & Services
15-10-2015 (Thursday)	Software Process & Project Management (Elec - I)	Distributed Systems (Elec - I)	Information Security & Audit (Elec - I)	Distributed Systems (Elec - I)
17-10-2015 (Saturday)	Software Architecture and Design Patterns (Elec - II)	Software Architecture and Design Patterns (Elec - II)	Advanced Data Mining (Elec – II)	Data Ware housing & Mining (Elec - II)

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Nustulapur, Karimnagar – 510 481.

Date: 14-09-2015

## End Examination Time Table of M.Tech. I Year I Sem. (R13-Supple)-October-2015 Academic Year 2014-15

Examination Timings-02-00 PM To 05-00 PM

Date & Day	VLSI – DESIGN-57	DS & CE-06	EMB-SYS-55
05-10-2015 (Monday)	CPLD and FPGA Architectures and Applications	Digital System Design	Embedded System Design
07-10-2015 (Wednesday)	VLSI Technology & Design	VLSI Technology & Design	Embedded Real Time Operating Systems
09-10-2015 (Friday)	CMOS Analog Integrated Circuit Design	Advanced Data Communication	Embedded - C
13-10-2015 (Tuesday)	CMOS Digital Integrated Circuit Design	Microcontrollers for Embedded System Design	Microcontrollers for Embedded System Design
15-10-2015 (Thursday)	Digital System Design (Elec - I)	CMOS Analog Integrated Circuit Design (Elec - I)	VLSI Technology & Design (Elec - I)
17-10-2015 (Saturday)	Microcontrollers for Embedded System Design (Elec - II)	CMOS Digital Integrated Circuit Design (Elec - II)	Digital System Design (Elec - II)

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Date: 14-09-2015

**End Examination Time Table of**  
**M.Tech. I Year I Sem. (R13-Supple)-October-2015**  
**Academic Year 2014-15**

Examination Timings-02-00 PM To 05-00 PM

<b>Date &amp; Day</b>	<b>PE-43</b>	<b>EPS-07</b>
05-10-2015 (Monday)	Machine Modeling and Analysis	Advanced Power System Analysis
07-10-2015 (Wednesday)	Power Electronic Converters - I	Advanced Power System Protection
09-10-2015 (Friday)	Power Electronic Control of DC Drives	Renewable Energy Systems
13-10-2015 (Tuesday)	Modern Control Theory	Modern Control Theory
15-10-2015 (Thursday)	HVDC Transmission (Elec - I)	EHV AC Transmission
17-10-2015 (Saturday)	Special Machines (Elec - II)	HVDC Transmission

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