

JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE-27
Nustulapur, Karimnagar – 505 481

M.Tech. I - Year I - SEM. (2014 ADMITTED BATCH)
FIRST Mid-Examinations, JAN - 2015

Date & Day	CSE	CS	SE	CNIS	Examination Timings
19-01-2015 (Monday)	Computer System Design	Computer System Design	Web Technologies & Services	Web Technologies & Services	10-00 A.M. To 12-00 Noon.
20-01-2015 (Tuesday)	Advanced Operating Systems	Advanced Operating Systems	Software Requirements & Estimation	Advanced Computer Networks	10-00 A.M. To 12-00 Noon.
21-01-2015 (Wednesday)	Distributed Systems	Distributed Systems (Elec – I)	Software Development Methodologies	Distributed Systems (Elec – I)	10-00 A.M. To 12-00 Noon
22-01-2015 (Thursday)	Software Process & Project Management (Elec-I)	Software Process & Project Management	Information Security & Audit (Elec – I)	Principles of Information Security	10-00 A.M. To 12-00 Noon
23-01-2015 (Friday)	Software Architecture and Design Patterns (Elec-II)	Software Architecture and Design Patterns (Elec-II)	Advanced Data Mining (Elec – II)	Data Ware housing & Mining (Elec – II)	10-00 A.M. To 12-00 Noon.
24-01-2015 (Saturday)	Advanced Data Structures & Algorithms	Advanced Data Structures & Algorithms	Advanced Data Structures & Algorithms	Advanced Data Structures & Algorithms	10-00 A.M. To 12-00 Noon

Verified by

Exam Branch

CSE-HOD

Officer-In-charge of Exam Branch

DIRECTOR

PRINCIPAL

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Date & Day	VLSI – DESIGN	DSCE	EMB – SYS.	Examination Timings
19-01-2015 (Monday)	Digital System Design (Elec-I)	Digital System Design	Digital System Design (Elec-II)	10-00 A.M. To 12-00 Noon.
20-01-2015 (Tuesday)	CPLD and FPGA Architectures and Applications	Advanced Data Communication	Embedded Real Time Operating Systems	10-00 A.M. To 12-00 Noon.
21-01-2015 (Wednesday)	CMOS Digital Integrated Circuit Design	CMOS Digital Integrated Circuit Design (Elec-II)	Embedded - C	10-00 A.M. To 12-00 Noon
22-01-2015 (Thursday)	VLSI Technology & Design	VLSI Technology & Design	VLSI Technology & Design (Elec-I)	10-00 A.M. To 12-00 Noon
23-01-2015 (Friday)	Microcontrollers for Embedded System Design (Elec-II)	Microcontrollers for Embedded System Design	Microcontrollers for Embedded System Design	10-00 A.M. To 12-00 Noon.
24-01-2015 (Saturday)	CMOS Analog Integrated Circuit Design	CMOS Analog Integrated Circuit Design (Elec-I)	Embedded System Design	10-00 A.M. To 12-00 Noon

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Date & Day	PE	EPS	Examination Timings
19-01-2015 (Monday)	Machine Modeling and Analysis	Advanced Power System Analysis	10-00 A.M. To 12-00 Noon.
20-01-2015 (Tuesday)	Power Electronic Control of DC Drives	Advanced Power System Protection	10-00 A.M. To 12-00 Noon.
21-01-2015 (Wednesday)	Power Electronic Converters - I	Renewable Energy Systems	10-00 A.M. To 12-00 Noon
22-01-2015 (Thursday)	Modern Control Theory	Modern Control Theory	10-00 A.M. To 12-00 Noon
23-01-2015 (Friday)	HVDC Transmission (Elec-I)	HVDC Transmission (Elec-II)	10-00 A.M. To 12-00 Noon.
24-01-2015 (Saturday)	Special Machines (Elec-II)	EHV AC Transmission (Elec – I)	10-00 A.M. To 12-00 Noon

Verified by Exam Branch EEE-HOD Officer-In-charge of Exam Branch DIRECTOR PRINCIPAL