

JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE – KARIMNAGAR – 505 481.

MID-EXAM-TIME TABLE

FIRST MID-EXAMINATIONS OF III B.TECH. II SEM. (R09) FEB-2015

DATE & DAY	CSE	IT	ECE	EEE	Examination Timings
20-02-2015 (Friday)	Object Oriented Analysis and Design	Computer Graphics	Digital Communications	Electrical Measurements	10-00 A.M. To 11-30 A.M.
	VLSI Design	Embedded Systems	Digital Signal Processing	Power Semiconductor	02-00 P.M. To 03-30 P.M.
21-02-2015 (Saturday)	Compiler Design	Data Warehousing and Data Mining	***	Environmental Studies	10-00 A.M. To 11-30 A.M.
	Network Security	Network Security	Microprocessors and Microcontrollers	Microprocessors and Microcontrollers	02-00 P.M. To 03-30 P.M.
23-02-2014 (Monday)	Managerial Economics and Financial Analysis	Operations Research	Managerial Economics and Financial Analysis	Computer Methods in Power Systems	10-00 A.M. To 11-30 A.M.
	Web Technologies	Web Technologies	Operating Systems	Renewable Energy Sources	02-00 P.M. To 03-30 P.M.

Exam Branch Verified by CSE-HOD ECE-HOD EEE-HOD Officer-In-charge of Exam Branch DIRECTOR PRINCIPAL