

JYOTHISHMATHI INSTITUTE OF TECHNOLOGY AND SCIENCE, KARIMNAGAR

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

TIME TABLE FOR II B.TECH I SEM-CSE-A(2013-2014)

W.E.F.30/06/14

DAY	LHNO	8:45A	9:45AM	10:45A	11:45AM - 12:30PM	LHN O	12:30P	1:30PM	2:30PM
		M	M	M			M	M	M
		1	2	3			4	5	6
MONDAY	DS/ BEE LAB	DS LAB/BEE LAB			L U N C H	302	EDC	MFCS	DS
TUESDAY	DS/ BEE LAB	DS LAB/BEE LAB				302	EDC	DLD	BEE
WEDAY	DG-1	DLD-T	P&S-T	DS-T		302	P&S	EDC	MFCS
THURDAY	304	P&S	BEE	MFCS		304	DLD	DS	EDC
FRIDAY	304	MFCS	P&S	BEE		304	DLD	BEE	DS
SAT DAY	303	P&S	DLD	DS		DG-1	BEE-T	MFCS-T	EDC-T

SUBJECTCODE	SUBJECT NAME
DS	DATA STRUCTURES
EDC	ELECTRONIC DIGITAL CIRCUITS
MFCS	MATHEMATICAL FOUNDATIONS AND COMPUTER SCIENCE
BEE	BASIC ELECTRICAL ENGINEERING
DLD	DIGITAL LOGIC DESIGN
P & S	PROBABILITY & STATISTICS
DS LAB	DATA STRUCTURES LAB

BEE LAB	BASIC ELECTRICAL ENGINEERING LAB
---------	-------------------------------------

VERIFIED BY

COORDINATOR

HOD

PRINCIPAL