

JYOTHISHMATHI INSTITUTE OF TECHNOLOGY AND SCIENCE, KARIMNAGAR

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

TIME TABLE FOR II B.TECH I SEM-CSE-B(2013-2014)

W.E.F.30/06/14

DAY	LHNO	8:45A M	9:45AM	10:45A M	11:45A M- 12:30PM	LHNO	12:30P M	1:30P M	2:30P M
		9:45A M	10:45A M	11:45A M			1:30PM	2:30P M	3:30P M
		1	2	3			4	5	6
MONDAY	007	BEE	P&S	MFCS	L U N C H	DS/ BEE LAB	DS LAB/BEE LAB		
TUESDAY	DS/ BEE LAB	DS LAB/BEE LAB				220	EDC	DS	BEE
WEDAY	DG-1	DLD-T	P&S-T	DS-T		303	DLD	DS	EDC
THURDAY	303	EDC	DS	P&S		303	MFCS	P&S	DLD
FRIDAY	303	BEE	DLD	MFCS		303	BEE	P&S	DLD
SAT DAY	302	DS	MFCS	EDC		DG-1	BEE-T	MFCS-T	EDC-T

SUBJECTCODE	SUBJECT NAME
DS	DATA STRUCTURES
EDC	ELECTRONIC DIGITAL CIRCUITS
MFCS	MATHEMATICAL FOUNDATIONS AND COMPUTER SCIENCE
BEE	BASIC ELECTRICAL ENGINEERING
DLD	DIGITAL LOGIC DESIGN
P & S	PROBABILITY & STATISTICS
DS LAB	DATA STRUCTURES LAB

BEE LAB	BASIC ELECTRICAL ENGINEERING LAB
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VERIFIED BY

COORDINATOR

HOD

PRINCIPAL