

JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE, KARIMNAGAR
DEPARTMENT OF CSE

TIME TABLE FOR III- B.TECH II-SEMESTER- CSE - A (2014-2015)

W.E.F: 29-12-2014

DAY	LHNO	8.45AM	9.45AM	10.45A M	11.45A M	LHNO	12.30 PM	1.30P M	2.30PM
		9.45AM	10.45A M	11.45P M	12.30 PM		1.30PM	2.30P M	3.30PM
		1	2	3			4	5	6
MON DAY	DG-1	VLSI-T	MEFA- T	OOAD- T	L U N C H	220	WT	NS	VLSI
TUE DAY	DG-1	NS-T	CD-T	WT-T		219	OOAD	WT	MEFA
WED DAY	OO6	WT	NS	OOAD		OO6	MEFA	CD	VLSI
THU DAY	218	CD	OOAD	NS		WT&CD LAB/AEC S LAB	WT &CD LAB/AECSLAB		
FRI DAY	OO6	MEFA	NS	VLSI		OO6	CD	OOAD	WT
SAT DAY	WT&CD LAB/AEC S LAB	WT &CD LAB/AECSLAB				221	VLSI	MEFA	CD

S.no	Sub.code	Name of the subject / Lab
1	OOAD	OBJECT ORIENTED ANALYSIS AND DESIGN
2	VLSI	VLSI DESIGN
3	NS	NETWORK SECURITY
4	CD	COMPILER DESIGN
5	MEFA	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS
6	WT	WEB TECHNOLOGIES
7	AEC S LAB	ADVANCED ENGLISH COMMUNUCATION SKILLS LAB
8	WT&CD LAB	WEB TECHNOLOGIES AND COMPILER DESIGN LAB

VERIFIED BY

CO-ORDINATOR HOD

DIRECTOR

PRINCIPAL
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