

**JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE, KARIMNAGAR**  
**DEPARTMENT OF CSE**

**TIME TABLE FOR III- B.TECH II-SEMESTER- CSE- B (2014-2015)**

**W.E.F: 29-12-2014**

DAY	LHNO	8.45A M	9.45AM	10.45A M	11.45A M	LHNO	12.30 PM	1.30PM	2.30PM
		9.45A M	10.45A M	11.45P M	12.30 PM		1.30PM	2.30PM	3.30PM
		1	2	3			4	5	6
MON DAY	303	CD	WT	VLSI		DG1	MEFA- T	OOAD- T	NS-T
TUE DAY	303	OOAD	WT	CD	<b>L U N C H</b>	303	VLSI	NS	MEFA
WED DAY	304	NS	VLSI	CD		304	MEFA	OOAD	WT
THU DAY	WT&OOP LAB/AEC S LAB	WT &CD LAB/AEC SLAB				DG-1	CD-T	WT-T	VLSI-T
FRI DAY	220	VLSI	NS	CD		220	OOAD	NS	MEFA
SAT DAY	221	WT	MEFA	OOAD		WT&CD LAB/AEC S LAB	WT &CD LAB/AEC SLAB		

S.no	Sub.code	Name of the subject / Lab
1	OOAD	OBJECT ORIENTED ANALYSIS AND DESIGN
2	VLSI	VLSI DESIGN
3	NS	NETWORK SECURITY
4	CD	COMPILER DESIGN
5	MEFA	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS
6	WT	WEB TECHNOLOGIES
7	AEC S LAB	ADVANCED ENGLISH COMMUNUCATION SKILLS LAB
8	WT&CD LAB	WEB TECHNOLOGIES AND COMPILER DESIGN LAB

**VERIFIED BY**

**CO-ORDINATOR HOD**

**DIRECTOR**

**PRINCIPAL**  
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