

JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE, KARIMNAGAR
DEPARTMENT OF CSE

TIME TABLE FOR III- B.TECH II-SEMESTER- CSE- C (2014-2015)

W.E.F: 29-12-2014

DAY	LHNO	8.45AM	9.45AM	10.45A M	11.45A M	LHN O	12.30 PM	1.30PM	2.30PM
		9.45AM	10.45A M	11.45P M	12.30 PM		1.30P M	2.30PM	3.30PM
		1	2	3			4	5	6
MON DAY	DG-1	VLSI-T	MEFA-T	OOAD- T	L U N C H	221	CD	WT	OOAD
TUE DAY	DG-1	NS-T	CD-T	WT-T		221	CD	NS	MEFA
WED DAY	WT&CD LAB/AEC S LAB	WT &CD LAB/AEC SLAB				7	VLSI	MEFA	OOAD
THU DAY	WT&CD LAB/AEC S LAB	WT &CD LAB/AEC SLAB				219	VLSI	WT	NS
FRI DAY	221	CD	OOAD	WT		221	MEFA	VLSI	NS
SAT DAY	7	MEFA	NS	OOAD		7	CD	WT	VLSI

S.no	Sub.code	Name of the subject / Lab
1	OOAD	OBJECT ORIENTED ANALYSIS AND DESIGN
2	VLSI	VLSI DESIGN
3	NS	NETWORK SECURITY
4	CD	COMPILER DESIGN
5	MEFA	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS
6	WT	WEB TECHNOLOGIES
7	AEC S LAB	ADVANCED ENGLISH COMMUNUCATION SKILLS LAB
8	WT&CD LAB	WEB TECHNOLOGIES AND COMPILER DESIGN LAB

VERIFIED BY

CO-ORDINATOR

HOD

DIRECTOR

PRINCIPAL
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