

**END-EXAMINATION TIME TABLE**

OF

**M.Tech. I Year II Sem. (Supple)-Examinations, February-2014**

**Examination Timings: 02-00 A.M. To 05-00 P.M.**

<b>Date &amp; Day</b>	<b>CSE</b>	<b>SE</b>	<b>CN &amp; IS</b>	<b>CS</b>	<b>VLSI – DESIGN</b>	<b>EMB – SYS.</b>	<b>DS &amp; CE</b>
03-02-2014 (Monday)	Distributed Computing	Distributed Computing	Wireless Networks	Distributed Computing	CMOS Analog & Mixed Signal Design	CPLD and FPGA Architectures and Applications	Advanced Computer Architecture
05-02-2014 (Wednesday)	Distributed Databases	Image Processing and Pattern Recognition	Information Security-II	Distributed Databases	System on Chip Architecture	Hardware Software Co-design	System on Chip Architecture (Elec-III)
07-02-2014 (Friday)	Information Retrieval Systems (Elec-III)	Software Architecture and Design Patterns	Information Retrieval Systems (Elec-IV)	Information Retrieval Systems (Elec-III)	Embedded Real Time Operating Systems	Digital Signal Processors and Architectures	Embedded Real Time Operating Systems
10-02-2014 (Monday)	Wireless Networks & Mobile Computing (Elec-IV)	Service Oriented Architecture	Information Security Management and Standards	Wireless Network & Mobile Computing	Design Fault Tolerant Systems	System Modeling and Simulation	Design Fault Tolerant Systems
12-02-2014 (Wednesday)	Advanced Computer Architecture	Software Quality Assurance and Testing	Mobile Computing	Advanced Compiler Design	Low Power VLSI Design (Elec-IV)	Low Power VLSI Design(Elec-IV)	Low Power VLSI Design
14-02-2014 (Friday)	Advanced Computer Networks	Multimedia and Rich Internet Development	Distributed Systems	Advanced Computer Networks	Digital Signal Processors and Architectures (Elec-III)	System on Chip Architecture (Elec-IV)	Digital Signal Processors and Architectures (Elec-IV)