

JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE-27
Nustulapur, Karimnagar – 505 481.

Date: 22-01-2014

End Examination Time Table of
M.Tech. I Year I Sem. (R09-Supple) February-2014

Date & Day	CSE	SE	CS	CNIS
03-02-2014 (Monday)	Advanced Problem Solving	Software Requirement Estimation	Advanced Problem Solving	Network Programming
05-02-2014 (Wednesday)	Computer System Design	Software Process and Project Management	Computer System Design	Distributed Computing
07-02-2014 (Friday)	Embedded Systems	Distributed Database [Elect-II]	Embedded Systems	Information Security-I
10-02-2014 (Monday)	JAVA and Web Technologies	JAVA and Web Technologies	JAVA and Web Technologies	JAVA and Web Technologies [Elect-I]
12-02-2014 (Wednesday)	Object Oriented Modeling [Elect-I]	Object Oriented Modeling [Elect-I]	Object Oriented Modeling [Elect-I]	Data Warehousing & Mining [Elect-II]
14-02-2014 (Friday)	***	Advanced Computer Network [Elect-I]	***	Advanced Computer Networks
03-02-2014 (Monday)	Advanced Compiler Design	***	Software Design & Engineering	***

Verified by

CSE-HOD

Officer-In-charge of Exam Branch

DIRECTOR

PRINCIPAL

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M.Tech. I Year I Sem. (R09-Supple) February-2014**

Date & Day	VLSI – DESIGN	EMB – SYS.	DSCE	PE
03-02-2014 (Monday)	Digital System Design	Digital System Design [Elect-II]	Digital System Design	Machine Modeling and Analysis
05-02-2014 (Wednesday)	VLSI Technology & Design	VLSI Technology & Design	VLSI Technology & Design	Analysis of Power Electronic Converters
07-02-2014 (Friday)	***	Embedded Real Time Operating Systems	Advanced Data Communications	***
10-02-2014 (Monday)	Algorithms for VLSI Design & Automation	Advanced Computer Networks	***	Power Electronic Control of DC Drives
12-02-2014 (Wednesday)	CPLD and FPGA Architectures and Applications	***	CPLD and FPGA Architectures and Applications	Modern Control Theory
14-02-2014 (Friday)	Microcontrollers for Embedded System	Microcontrollers for Embedded System Design	Microcontrollers for Embedded System Design	Dynamics of Electrical Machines [Elect-II]
03-02-2014 (Monday)	Advanced Digital Signal Processing	Advanced Digital Signal Processing [Elect-I]	Advanced Digital Signal Processing [Elect-I]	Modern Power Electronics [Elect-I]

Verified by

ECE-HOD

EEE-HOD

Officer-In-charge of Exam Branch

DIRECTOR

PRINCIPAL