

JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE-27
Nustulapur, Karimnagar – 502 481.

Date: 22-01-2014

End Examination Time Table of
M.Tech. I Year I Sem. (R13-Regular) February-2014

Date & Day	CSE	SE	CS	CNIS
03-02-2014 (Monday)	Advanced Data Structures & Algorithms	Advanced Data Structures & Algorithms	Advanced Data Structures & Algorithms	Advanced Data Structures & Algorithms
05-02-2014 (Wednesday)	Computer System Design	Software Development Methodologies	Computer System Design	Principles of Information Security
07-02-2014 (Friday)	Distributed Systems	Software Requirements & Estimation	Software Process & Project Management	Advanced Computer Networks
10-02-2014 (Monday)	Advanced Operating Systems	Web Technologies & Services	Advanced Operating Systems	Web Technologies & Services
12-02-2014 (Wednesday)	Software Process & Project Management (Elec - I)	Information Security & Audit (Elec - I)	Distributed Systems (Elec - I)	Distributed Systems (Elec - I)
14-02-2014 (Friday)	Software Architecture and Design Patterns (Elec - II)	Advanced Data Mining (Elec – II)	Software Architecture and Design Patterns (Elec - II)	Data Ware housing & Mining (Elec - II)

Exam Branch

Verified by

CSE-HOD

Officer-In-charge of
Exam Branch

DIRECTOR

PRINCIPAL

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M.Tech. I Year I Sem. (R13-Regular) February-2014**

Date & Day	VLSI – DESIGN	EMB – SYS.	DSCE	PE
03-02-2014 (Monday)	CPLD and FPGA Architectures and Applications	Embedded System Design	Digital System Design	Machine Modeling and Analysis
05-02-2014 (Wednesday)	VLSI Technology & Design	Embedded Real Time Operating Systems	VLSI Technology & Design	Power Electronic Converters - I
07-02-2014 (Friday)	CMOS Analog Integrated Circuit Design	Embedded - C	Advanced Data Communication	Power Electronic Control of DC Drives
10-02-2014 (Monday)	CMOS Digital Integrated Circuit Design	Microcontrollers for Embedded System Design	Microcontrollers for Embedded System Design	Modern Control Theory
12-02-2014 (Wednesday)	Digital System Design (Elec - I)	VLSI Technology & Design (Elec - I)	CMOS Analog Integrated Circuit Design (Elec - I)	HVDC Transmission (Elec - I)
14-02-2014 (Friday)	Microcontrollers for Embedded System Design (Elec - II)	Digital System Design (Elec - II)	CMOS Digital Integrated Circuit Design (Elec - II)	Special Machines (Elec - II)

Exam Branch

Verified by

ECE-HOD

Officer-In-charge of
Exam Branch

DIRECTOR

PRINCIPAL