

JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE-27
Nustulapur, Karimnagar – 510 481.

Date: 02-02-2016

End Examination Time Table of
M.Tech. I Year I Sem. (R15-Regular)(2015 Batch) February-2016
Academic Year 2015-16

Examination Timings-10-00 AM To 01-00 PM

Date & Day	CSE-58	CS-05	CNIS-78
08-02-2016 (Monday)	Data Structures and Algorithms	Data Structures and Algorithms	Data Structures and Algorithms
10-02-2016 (Wednesday)	Database Internals	Database Internals	Computer Networking
12-02-2016 (Friday)	Distributed Systems	Distributed Systems	Network Programming
15-02-2016 (Monday)	Network Security (Elec-C1)	Network Security (Elec-C1)	Cloud Computing (Elec-C1)
17-02-2016 (Wednesday)	***	***	Distributed Systems (Elec-C2)
19-02-2016 (Friday)	Software Engineering(Open Elect	Software Engineering(Open Elec)	Principles of Information Security (Open Elec)
22-02-2016 (Monday)	Software Architecture and Design Patterns (Elec-C2)	Software Architecture and Design Patterns (Elec-C2)	***

Verified by

CSE-HOD

Officer-In-charge of Exam Branch

DIRECTOR

PRINCIPAL

**End Examination Time Table of
M.Tech. I Year II Sem. (R13-Regular)(2014 Batch) October-2016
Academic Year 2014-15**

Examination Timings-10-00 AM To 01-00 PM

Date & Day	VLSI – DESIGN-57	DS & CE-06	EMB-SYS-55
05-10-2016 (Monday)	Low Power VLSI Design	Advanced Computer Architecture	Hardware Software Co – Design
07-10-2016 (Wednesday)	CMOS Mixed Signal Circuit Design	Low Power VLSI Design	CPLD & FPGA Architectures & Applications
09-10-2016 (Friday)	CAD for VLSI Circuits	Embedded Real Time Operating Systems	Embedded Networking
13-10-2016 (Tuesday)	Design for Testability	Design for Testability	Digital Signal Processors & Architectures
15-10-2016 (Thursday)	Digital Signal Processors & Architectures (Elec – III)	System on Chip Architecture (Elec-III)	Wireless Communications & Networks (Elec – III)
17-10-2016 (Saturday)	System on Chip Architecture (Elec – IV)	Digital Signal Processors & Architectures (Elec – IV)	System on Chip Architecture (Elec – IV)

Verified by

ECE-HOD

Officer-In-charge of Exam Branch

DIRECTOR

PRINCIPAL

**End Examination Time Table of
M.Tech. I Year II Sem. (R13-Regular)(2014 Batch) October-2016
Academic Year 2014-15**

Examination Timings-10-00 AM To 01-00 PM

Date & Day	PE-43	EPS-07
05-10-2016 (Monday)	Power Electronic Converters – II	Power System Dynamics
07-10-2016 (Wednesday)	Flexible AC Transmission Systems (FACTS)	Flexible AC Transmission Systems (FACTS)
09-10-2016 (Friday)	Power Electronic Control of AC Drives	Power System Operation & Deregulation
13-10-2016 (Tuesday)	Neural Networks & Fuzzy Systems	AI Techniques in Electrical Power Engineering
15-10-2016 (Thursday)	Digital Control Systems (Elec – III)	Gas Insulated Systems (Elec – III)
17-10-2016 (Saturday)	Renewable Energy Systems (Elec – IV)	Power System Reliability (Elec – IV)

Verified by

EEE-HOD

Officer-In-charge of Exam Branch

DIRECTOR

PRINCIPAL

JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE-27

Nustulapur, Karimnagar – 510 481.

Date: 02-02-2016

End Examination Time Table of M.Tech. I Year I Sem. (R13-Supple)-February-2016 Academic Year 2015-16

Examination Timings-10-00 AM To 01-00 PM

Date & Day	CSE-58	CS-05	SE-25	CNIS-78
08-02-2016 (Monday)	Advanced Data Structures & Algorithms	Advanced Data Structures & Algorithms	Advanced Data Structures & Algorithms	Advanced Data Structures & Algorithms
10-02-2016 (Wednesday)	Computer System Design	Computer System Design	Software Development Methodologies	Principles of Information Security
12-02-2016 (Friday)	Distributed Systems	Software Process & Project Management	Software Requirements & Estimation	Advanced Computer Networks
15-02-2016 (Monday)	Advanced Operating Systems	Advanced Operating Systems	Web Technologies & Services	Web Technologies & Services
17-02-2016 (Wednesday)	Software Process & Project Management (Elec - I)	Distributed Systems (Elec - I)	Information Security & Audit (Elec - I)	Distributed Systems (Elec - I)
19-02-2016 (Friday)	Software Architecture and Design Patterns (Elec - II)	Software Architecture and Design Patterns (Elec - II)	Advanced Data Mining (Elec - II)	Data Warehousing & Mining (Elec - II)

Exam Branch

Verified by

Officer-In-charge of Exam Branch

DIRECTOR

PRINCIPAL

**End Examination Time Table of
M.Tech. I Year I Sem. (R13-Supple)-February-2016
Academic Year 2015-16**

Examination Timings-02-00 PM To 05-00 PM

Date & Day	VLSI – DESIGN-57	DS & CE-06	EMB-SYS-55
08-02-2016 (Monday)	CPLD and FPGA Architectures and Applications	Digital System Design	Embedded System Design
10-02-2016 (Wednesday)	VLSI Technology & Design	VLSI Technology & Design	Embedded Real Time Operating Systems
12-02-2016 (Friday)	CMOS Analog Integrated Circuit Design	Advanced Data Communication	Embedded - C
15-02-2016 (Monday)	CMOS Digital Integrated Circuit Design	Microcontrollers for Embedded System Design	Microcontrollers for Embedded System Design
17-02-2016 (Wednesday)	Digital System Design (Elec - I)	CMOS Analog Integrated Circuit Design (Elec - I)	VLSI Technology & Design (Elec - I)
19-02-2016 (Friday)	Microcontrollers for Embedded System Design (Elec - II)	CMOS Digital Integrated Circuit Design (Elec - II)	Digital System Design (Elec - II)

Exam Branch

Verified by

Officer-In-charge of Exam Branch

DIRECTOR

PRINCIPAL

JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE-27
Nustulapur, Karimnagar – 510 481.

Date: 14-09-2016

End Examination Time Table of
M.Tech. I Year I Sem. (R13-Supple)-February-2016
Academic Year 2015-16

Examination Timings-02-00 PM To 05-00 PM

Date & Day	PE-43	EPS-07
08-02-2016 (Monday)	Machine Modeling and Analysis	Advanced Power System Analysis
10-02-2016 (Wednesday)	Power Electronic Converters - I	Advanced Power System Protection
12-02-2016 (Friday)	Power Electronic Control of DC Drives	Renewable Energy Systems
15-02-2016 (Monday)	Modern Control Theory	Modern Control Theory
17-02-2016 (Wednesday)	HVDC Transmission (Elec - I)	EHV AC Transmission
19-02-2016 (Friday)	Special Machines (Elec - II)	HVDC Transmission

Exam Branch

Verified by

Officer-In-charge of Exam Branch

DIRECTOR

PRINCIPAL

JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE-27

Nustulapur, Karimnagar – 510 481.

Date: 02-02-2016

End Examination Time Table of M.Tech. I Year II Sem. (R13-Supple)-February-2016 Academic Year 2015-16

Examination Timings-02-00 PM To 05-00 PM

Date & Day	CSE-58	CS-05	SE-25	CNIS-78
08-02-2016 (Monday)	Advanced Network Programming	Advanced Network Programming	Software Architecture and Design Patterns	Wireless Networks & Mobile Computing
10-02-2016 (Wednesday)	Advanced Databases	Web Services and Service Oriented Architecture	Software Process & Project Management	Applications of Networks Security
12-02-2016 (Friday)	Web Services and Service Oriented Architecture	Advanced Databases	Software Quality Assurance & Testing	Information Security Management & Standard
15-02-2016 (Monday)	Grid and Cloud Computing	Wireless Networks & Mobile Computing	Component Based Software Engineering	Ad hoc and Sensor Networks
17-02-2016 (Wednesday)	Advanced Data Mining (Elec – III)	Advanced Data Mining (Elec – III)	Information Retrieval Systems (Elec – III)	Mobile Application Development using J2ME and Android (Elec – III)
19-02-2016 (Friday)	Information Retrieval Systems (Elec – IV)	Information Retrieval Systems (Elec – IV)	Cloud Computing (Elec – IV)	Cloud Computing (Elec – IV)

Exam Branch

Verified by

Officer-In-charge of Exam Branch

DIRECTOR

PRINCIPAL

JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE-27

Nustulapur, Karimnagar – 510 481.

Date: 02-02-2016

End Examination Time Table of M.Tech. I Year II Sem. (R13-Supple)-February-2016 Academic Year 2015-16

Examination Timings-02-00 PM To 05-00 PM

Date & Day	VLSI – DESIGN-57	DS & CE-06	EMB-SYS-55
08-02-2016 (Monday)	Low Power VLSI Design	Advanced Computer Architecture	Hardware Software Co – Design
10-02-2016 (Wednesday)	CMOS Mixed Signal Circuit Design	Low Power VLSI Design	CPLD & FPGA Architectures & Applications
12-02-2016 (Friday)	CAD for VLSI Circuits	Embedded Real Time Operating Systems	Embedded Networking
15-02-2016 (Monday)	Design for Testability	Design for Testability	Digital Signal Processors & Architectures
17-02-2016 (Wednesday)	Digital Signal Processors & Architectures (Elec – III)	System on Chip Architecture (Elec-III)	Wireless Communications & Networks (Elec – III)
19-02-2016 (Friday)	System on Chip Architecture (Elec – IV)	Digital Signal Processors & Architectures (Elec – IV)	System on Chip Architecture (Elec – IV)

Exam Branch

Verified by

Officer-In-charge of Exam Branch

DIRECTOR

PRINCIPAL

JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE-27
Nustulapur, Karimnagar – 510 481.

Date: 02-02-2016

End Examination Time Table of
M.Tech. I Year II Sem. (R13-Supple)-February-2016
Academic Year 2015-16

Examination Timings-02-00 PM To 05-00 PM

Date & Day	PE-43	EPS-07
08-02-2016 (Monday)	Power Electronic Converters – II	Power System Dynamics
10-02-2016 (Wednesday)	Flexible AC Transmission Systems (FACTS)	Flexible AC Transmission Systems (FACTS)
12-02-2016 (Friday)	Power Electronic Control of AC Drives	Power System Operation & Deregulation
15-02-2016 (Monday)	Neural Networks & Fuzzy Systems	AI Techniques in Electrical Power Engineering
17-02-2016 (Wednesday)	Digital Control Systems (Elec – III)	Gas Insulated Systems (Elec – III)
19-02-2016 (Friday)	Renewable Energy Systems (Elec – IV)	Power System Reliability (Elec – IV)

Exam Branch

Verified by

Officer-In-charge of Exam Branch

DIRECTOR

PRINCIPAL