



JYOTISHMATHI INSTITUTE OF TECHNOLOGY AND SCIENCE

Nustulapur, Karimnagar – 505 481 (TS)
(Approved by AICTE, New Delhi and Affiliated to JNTUH)
An ISO 9001:2015 Certified Institution

Date: 11-04-2020

ONLINE CLASS SCHEDULE FOR I YEAR II SEMESTER

DAY	YEAR-SEM	CLASS	10.00AM TO 11.30 AM	2.00 PM TO 3.30PM
			1	2
MON DAY	II-II	EEE-A	Laplace Transform and Numerical Method (Dr.Ramesh)	Control systems(Dr.M.Manikandan)
		EEE-B	Power system-I(T.Praveen kumar)	Control systems(R.Babu)
	III-II	EEE	Power Electronics(S.Janardhan)	Intellectual property rights(Mr.Ravichandar)
	IV-II	EEE-A	High voltage engineering (Dr.G.Sridhar)	
		EEE-B	High voltage engineering (Dr.G.Sridhar)	
TUE DAY	II-II	EEE-A	Power system-I(S.Gopal Reddy)	Digital logic Circuit(Sriramulu)
		EEE-B	Laplace Transform and Numerical Method (srinavas)	Digital logic Circuit(Sriramulu)
	III-II	EEE	Power system Analysis (P.Balakishan)	Electrical and Electronics Instrumentation (T.Praveen kumar)
	IV-II	EEE-A	Environmental Impact Assessment(G.Naveena)	
		EEE-B	Environmental Impact Assessment(G.Naveena)	
WED DAY	II-II	EEE-A	Electrical Machines-II (Ch.Sajan Rao)	Laplace Transform and Numerical Method (Dr.Ramesh)
		EEE-B	Electrical Machines-II (Viswaprakesh Babu)	Laplace Transform and Numerical Method (srinavas)
	III-II	EEE	Electrical and Electronics Instrumentation (T.Praveen kumar)	Power Electronics(S.Janardhan)
	IV-II	EEE-A	Utilization of Electrical Energy(S.Janardhan)	
		EEE-B	Utilization of Electrical Energy(P.Balakishan)	
THU DAY	II-II	EEE-A	Digital logic Circuit(Sriramulu)	Electrical Machines-II (Ch.Sajan Rao)
		EEE-B	Digital logic Circuit(Sriramulu)	Electrical Machines-II (Viswaprakesh Babu)
	III-II	EEE	Intellectual property rights(Mr.Ravichandar)	Switchgear and Protection (S.Gopal Reddy)

	IV-II	EEE-A	Utilization of Electrical Energy(S.Janardhan)	
		EEE-B	Utilization of Electrical Energy(P.Balakishan)	
FRI DAY	II-II	EEE-A	Control systems(Dr.M.Manikandan)	Laplace Transform and Numerical Method (Dr.Ramesh)
		EEE-B	Control systems(R.Babu)	Laplace Transform and Numerical Method (srinavas)
	III-II	EEE	Switchgear and Protection (S.Gopal Reddy)	Power system Analysis (P.Balakishan)
	IV-II	EEE-A	Environmental Impact Assessment(G.Naveena)	
		EEE-B	Environmental Impact Assessment(G.Naveena)	
SAT DAY	II-II	EEE-A	Power system-I(S.Gopal Reddy)	
		EEE-B	Power system-I(T.Praveen kumar)	
	III-II	EEE	Power Electronics(S.Janardhan)	
	IV-II	EEE-A	High voltage engineering (Dr.G.Sridhar)	Project(Dr.M.Manikandan)
		EEE-B	High voltage engineering (Dr.G.Sridhar)	Project(P.Balakishan)