

JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE-27  
Nustulapur, Karimnagar-505527

Date:- 25-07-2024

EXAMINATION BRANCH

Lab External Examinations-July/Aug-2024  
I-M.Tech-II-Sem (J-23) [2023-24]

SPECIALIZATION-Computer Science & Engineering

DATE	LAB NAME	TIMINGS
30-07-2024 Tuesday	ADVANCED ALGORITHMS LAB	10-00 AM To
31-07-2024 Wednesday	ADVANCED COMPUTER NETWORKS LAB	01-00 PM

SPECIALIZATION-VLSI Design

DATE	LAB NAME	TIMINGS
01-08-2024 Thursday	VLSI ADVANCED PHYSICAL DESIGN LAB	10-00 AM To
02-08-2024 Friday	SYSTEM VERILOG TEST BENCH USING UVM LAB	01-00 PM

S.No.	VLSI- Design	CSE
1	23271D5701	23271D5801
2	23271D5702	23271D5802
3	23271D5703	23271D5803
4	23271D5704	23271D5804
5	23271D5705	*
6	23271D5706	*
7	23271D5707	*
8	23271D5708	*

  
Controller of Exams

  
DEAN-Exams

  
PRINCIPAL  
Principal  
Jyothishmathi Institute of  
Technology & Science  
Karimnagar.