



**JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE (AUTONOMOUS)**

Approved by AICTE, New Delhi & Affiliated to JNTUH, Hyderabad

Accredited with NAAC 'A' Grade | ISO 9001:2015 Certified

UG - CSE,ECE,EEE Accredited by National Board of Accreditation, New Delhi



Date:- 08-07-2024

**I-M.TECH-II-SEM (J-23) [2023-204]  
Second Mid Examinations-July-2024**

Date & Day		CSE-58	VLSI-System Design-57	Timings
22-07-2024	Monday	ADVANCED ALGORITHMS	VLSI ADVANCED PHYSICAL DESIGN	
23-07-2024	Tuesday	ADVANCED COMPUTER ARCHITECTURES	SYSTEM VERILOG TEST BENCH USING UVM	10-00 AM To 12-00 Noon
24-07-2024	Wednesday	ADVANCED COMPUTER NETWORKS	DESIGN FOR TESTABILITY	
25-07-2024	Thursday	NATURE INSPIRED COMPUTING	DEVICE MODELING	

**Controller of Exams**

**DEAN-Exams**

**PRINCIPAL**

**Jyothishmathi Institute of  
Technology & Science  
Karimnagar.**

Copy to:

1. Chairman Garu
2. Secretary Garu
3. Dean Exams
4. All the Heads of Departments.
5. File.