Date:- 22-07-2024

EXAMINATION BRANCH

M.TECH-II- SEMESTER-(J-23) REGULATIONS – REGULAR/SUPPLEMENTARY EXAMINATIONS AUGUST-2024

T I M E T A B L E

Timings: 10-00 AM To 01-00 PM

DATE	DAY	COMPUTER SCIENCE & ENGINEERING	VLSI DESIGN
05-08-2024	Monday	Advanced Algorithms-23CSPA03	VLSI Advanced Physical Design-23VSPA03
07-08-2024	Wednesday	Advanced Computer Architecture-23CSPA04	System Verilog Test Benches usingUVM-23VSPA04
09-08-2024	Friday	Advanced Computer Networks-23CSPE32	Design for Testability-23VSPE33
13-08-2024	Tuesday	Nature Inspired Computing-23CSPE42	Device Modelling-23VSPE41

M.TECH-I SEMESTER-(J-23) REGULATIONS – SUPPLEMENTARY EXAMINATIONS AUGUST-2024

Timings: 10-00 AM To 01-00 PM

DATE	DAY	COMPUTER SCIENCE & ENGINEERING	VLSI DESIGN
06-08-2024	Tuesday	Mathematical Foundations of Computer Science-23CSPA01	Digital System Design with FPGAs-23VSPA01
08-08-2024	Thursday	Advanced Data Structures-23CSPA02	CMOS Analog IC Design-23VSPA02
12-08-2024	Monday	Database Programming with PL/SQL-23CSPE11	Memory Technologies-23VSPE13
14-08-2024	Wednesday	Software Quality Engineering-23CSPE22	Communication Buses & Interfaces-23VSPE21

Controller of Exams

DEAN-Exams

PRINCIPAL

Principal
Jyothishmathi Institute of
Technology & Science
Karimnagar.