



JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE (AUTONOMOUS)

Approved by AICTE, New Delhi & Affiliated to JNTUH, Hyderabad

Accredited with NAAC 'A' Grade | ISO 9001:2015 Certified

UG - CSE,ECE,EEE Accredited by National Board of Accreditation, New Delhi



Date:- 03-12-2024

[AY: 2024-25]

I-M.TECH-I-SEM (J-23) (2024 Admitted Batch)

First Mid Examinations-December-2024

Date & Day		SPECIALIZATIONS		Timings
		Computer Science & Engineering	VLSI-System Design	
11-12-2024	Wednesday	MATHEMETICAL FOUNDATIONS OF COMPUTER SCIENCE	DIGITAL SYSTEM DESIGN WITH FPGAS	10-00 AM To 12-00 Noon
12-12-2024	Thursday	ADVANCED DATA STRUCTURES	CMOS ANALOG IC DESIGN	10-00 AM To 12-00 Noon
		DATABASE PROGRAMMING WITH PL/SQL	MEMORY TECHNOLOGIES	02-00 PM To 04-00 PM
13-12-2024	Friday	SOFTWARE QUALITIES ENGINEERING	COMMUNICATION BUSES & INTERFACES	10-00 AM To 12-00 Noon
		RESEARCH METHODOLOGY & IPR	RESEARCH METHODOLOGY & IPR	02-00 PM To 04-00 PM

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Controller of Exams

[Signature]
DEAN-Exams

[Signature]
PRINCIPAL
Principal
Jyothishmathi Institute of
Technology & Science
Karimnagar.

Copy to:

1. Chairman Garu
2. Secretary Garu
3. Dean Exams