

JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE (AUTONOMOUS)

Approved by AlCTE, New Delhi & Affillated to JNTUH, Hyderabad Accredited with NAAC 'A' Grade | ISO 9001:2015 Certified UG - CSE,ECE,EEE Accredited by National Board of Accreditation, New Delhi







Date:- 02-09-2025

II-B.Tech-I-Sem (J-23) Students [2025-26]

Class Test-II September-2025

DATE	ECE	EEE	, CSE	CSE-AIML	Timings
08-09-2025 Monday	Numerical Methods and Complex Variables		Digital Electronics	Mathematical and Statistical Foundations	10-45 AM To 11-45 AM
	Analog Circuits	Power System-I	Data Structures		02-30- PM To 03-30 PM
09-09-2025 Tuesday	Network analysis and Synthesis	Analog Electronic Circuits	Computer Organization and Architecture		10-45 AM To 11-45 AM
	Digital Logic Design	Electrical Machines-I	Computer Oriented Statistical Methods	Software Engineering	02-30- PM To 03-30 PM
10-09-2025 Wednesday	Signals and Systems	Electro Magnetic Fields	Object Oriented Programming through Java	Operating Systems	10-45 AM To 11-45 AM

Controller of Exams

Copy to

Chairman Sir

Secretary Sir

Dean Stduents Affair

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AN-Exams

PRINCIPAL
Principal

Jyothishmathi Institute
Technology & Science
Karimnagar.