JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE (AUTONOMOUS)
Approved by AICTE, New Delhi & Affiliated to JNTUH, Hyderabad
Accredited with NAAC 'A' Grade | ISO 9001:2015 Certified
UG - CSE,ECE,EEE Accredited by National Board of Accreditation, New Delhi

Date:- 16-09-2025

II-B.Tech-I-Sem (J-23) Students [AY:2025-26]
Revised First Mid Examinations-September-2025

DATE	ECE	EEE	CSE	CSE-AIML	Timings
17-09-2025 Wednesday	Numerical Methods and Complex Variables		Digital Electronics	Mathematical and Statistical Foundations	10-00 AM To 12-00 PM
	Analog Circuits	Power System-I	Data Structures		02-00- PM To 04-00 PM
18-09-2025 Thursday	Network analysis and Synthesis	Analog Electronic Circuits	Computer Organization and Architecture		10-00 AM To 12-00 PM
	Digital Logic Design	Electrical Machines-I	Computer Oriented Statistical Methods	Software Engineering	02-00- PM To 04-00 PM
19-09-2025 Friday	Signals and Systems	Electro Magnetic Fields	Object Oriented Programming through Java	Operating Systems	10-00 AM To 12-00 PM

Controller of Exams   DEAN-Exams   PRINCIPAL

Principal
Jyothishmathi Institute of
Technology & Scien
Karimnagar.