

## (Approved by AICTE, New Delhi & Affiliated to JNTUH, Hyderabad) JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE (AUTONOMOUS)

UG - CSE,ECE,EEE Accredited by National Board of Accreditation, New Delhi) (Accredited with NAAC 'A' Grade | ISO 9001:2015 Certified









Date:- 10-11-2025

## II-B.Tech-I-Sem (J-23) Students [2025-26] Class Test-IV November-2025

DATE         ECE         EEE         CSE         CSE-AIML           13-11-2025         Thursday         Analog Circuits         Power System-I         Digital Electronics         Mathematical and Statistical Foundations           14-11-2025         Friday         Network analysis and Synthesis         Analog Electronic Circuits         Computer Organization and Architecture           15-11-2025         Digital Logic Design         Electrical Machines-I         Computer Oriented Statistical Methods         Software Engineering           15-11-2025         Signals and Systems         Electro Magnetic Fields         Object Oriented Statistical Methods         Object Oriented Statistical Methods <td rows<="" th=""><th></th><th></th><th></th><th></th><th></th><th></th></td>	<th></th> <th></th> <th></th> <th></th> <th></th> <th></th>						
Numerical Methods and Complex Variables  Analog Circuits  Power System-I  Network analysis and Synthesis  Digital Logic Design  Electrical Machines-I  Computer Oriented Statistical Methods  Computer Oriented Statistical Methods	Systems	Operating	Object Oriented Programming through Java	Electro Magnetic Fields	Signals and Systems	15-11-2025 Saturday	
Numerical Methods and Complex Variables  Analog Circuits  Power System-I  Network analysis and Synthesis  Analog Electronic Circuits  Computer Organization	ngineering	Software Er	Computer Oriented Statistical Methods	Electrical Machines-I	Digital Logic Design	Friday	
Numerical Methods and Complex Variables  Analog Circuits  Power System-I  Data Str	ture	on and Architec	Computer Organizati	Analog Electronic Circuits	Network analysis and Synthesis	14-11-2025	
Numerical Methods and Complex Variables  Digital Electronics		uctures	Data Str	Power System-I	Analog Circuits	тинэшу	
ECE EEE CSE	nd Statistical	Mathematical a Founda	Digital Electronics	nd Complex Variables	Numerical Methods a	13-11-2025	
	IML	CSE-AIML	CSE	EEE	ECE	DATE	

Controller of Exams

Dean Stduents Affair Secretary Sir Chairman Sir

Notice Board All HOD's

Jyothishmathi Institute of Technology & Science Karimnagar