



JYOTHISHMATHI INSTITUTE OF TECHNOLOGY & SCIENCE (AUTONOMOUS)

(Approved by AICTE, New Delhi & Affiliated to JNTUH, Hyderabad)

(Accredited with NAAC 'A' Grade | ISO 9001:2015 Certified)

UG - CSE, ECE, EEE Accredited by National Board of Accreditation, New Delhi)



Date:- 23-01-2026

I-M.TECH-I-SEM (J-25) [2025-26] Second Mid Examinations-February-2026

Date & Day		CSE-58	VLSI-System Design-57	Timings
02-02-2026	Monday	Mathematical Foundations of Computer Science	CMOS Digital IC Design	10-00 AM To 12-00 PM
		Advanced Data Structures	CMOS Analog IC Design	02-00- PM To 04-00 PM
03-02-2026	Tuesday	Database Programming with PL SQL	Device Modeling	10-00 AM To 12-00 PM
		Software Quality Engineering	VLSI Architectures for Digital Signal Processing	02-00- PM To 04-00 PM
04-02-2026	Wednesday	Research Methodology & IPR	Research Methodology & IPR	10-00 AM To 12-00 PM

Controller of Exams

DEAN-Exams

PRINCIPAL

Principal
Jyothishmathi Institute of
Technology & Science
Karimnagar.

Copy to:

1. Chairman Sir
2. Secretary Sir
3. Dean Exams
4. All the Heads of Departments.
5. File.